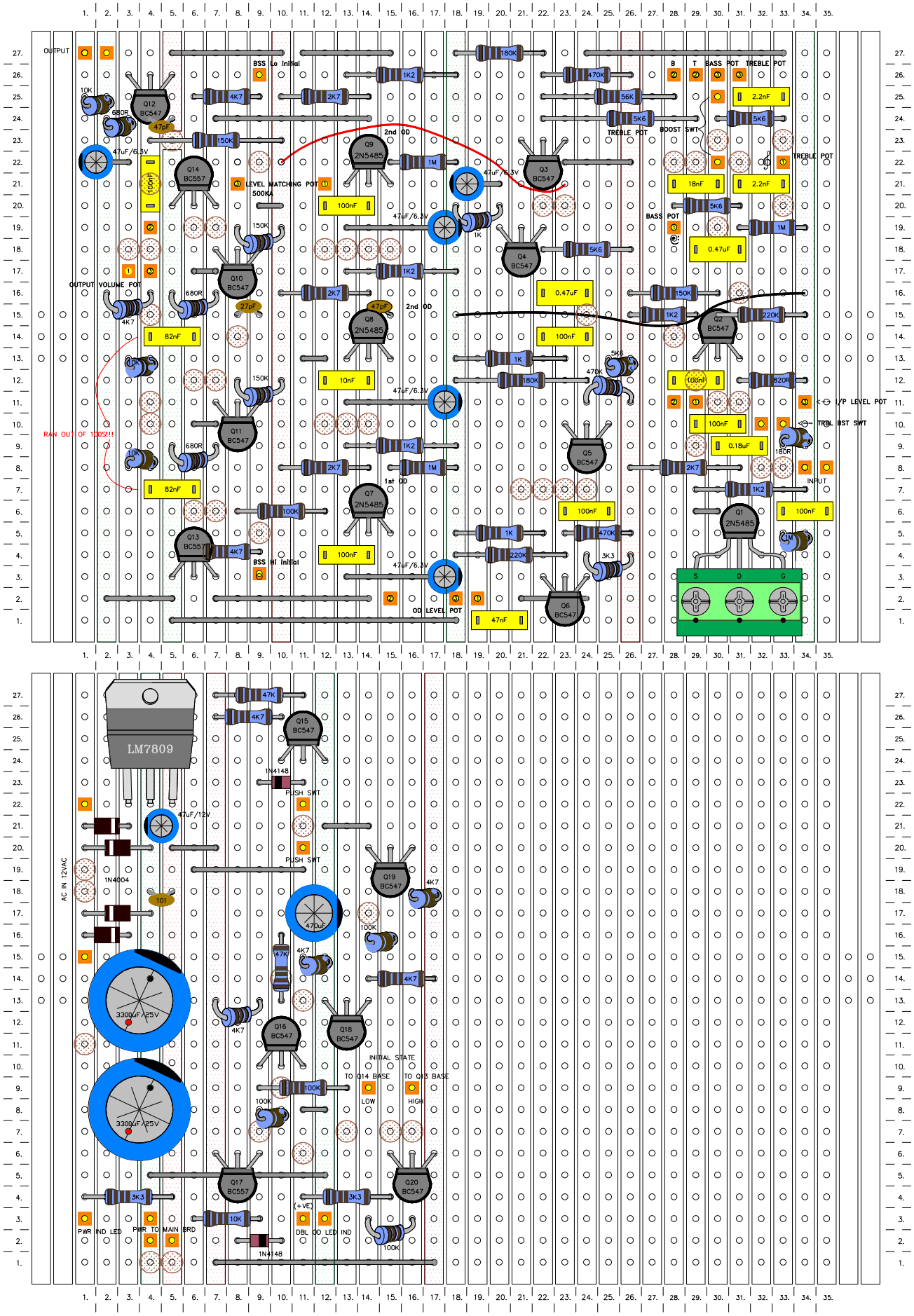
ACTUAL BUILD DESIGN DECEMBER 2016

* Source resistors on 2N5485s changed from 680R to 1K2 October 2021
Treble boost circuit on I/P FET changed from 150nF/470R to 0.18uF/180R